

U.S. Patent Application Serial No. **10/717,591**  
Amendment filed September 29, 2006  
Reply to OA dated May 30, 2006

**AMENDMENTS TO THE CLAIMS:**

Claims 1, 4-7, and 17 are presented for examination. Claims 2-3 were cancelled in a previous amendment. Claims 8-16 are withdrawn. New claim 17 contains identical subject matter as previously cancelled claim 3. Claim 1 has been amended. This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

**Claim 1 (Currently Amended):** An electronic parts packaging structure comprising:  
a mounted body on which electronic parts are mounted;  
the electronic parts having a connection pad formed on a surface side of the electronic parts;  
the electronic parts being mounted on the mounted body to direct the connection pad upward,  
the connection pad being constructed of an aluminum pad and an etching stopper film  
covering the aluminum pad;  
the aluminum pad consisting of aluminum or an aluminum alloy;  
the etching stopper film being constructed of a laminated film having layers of film; the  
~~laminated film having an etching stopper film as an uppermost film;~~ the uppermost film of the  
etching stopper film being selected from a member selected from a group consisting of a copper  
film, a gold film, a silver film, and a conductive paste film;  
an insulating film for covering the electronic parts;  
a via hole formed located at least in a predetermined portion of the insulating film on the

U.S. Patent Application Serial No. **10/717,591**  
Amendment filed September 29, 2006  
Reply to OA dated May 30, 2006

connection pad of the electronic parts; and

a wiring pattern connected to the connection pad via the via hole,

wherein the via hole is connected to the aluminum layer pad via the etching stopper film ~~the etching stopper film functions as a stopper in case the via hole is formed by a laser, the etching stopper film being a member selected from a group consisting of a copper film, a gold film, a silver film, and a conductive paste film.~~

**Claims 2-3 (Cancelled):**

**Claim 4 (Previously Presented):** The electronic parts packaging structure according to claim 1, wherein the mounted body is a base substrate having a wiring pattern thereon or a structural body in which an insulating film and a wiring pattern are laminated by a predetermined number on the base substrate, and the wiring pattern connected to the connection pad is connected electrically to the wiring patterns under the electronic parts via via holes formed in the insulating films.

**Claim 5 (Previously Presented):** The electronic parts packaging structure according to claim 4, wherein a plurality of electronic parts are packaged three-dimensionally in a state that the electronic parts are buried in a plurality of insulating films, and the plurality of electronic parts are connected mutually via the via holes formed in the insulating films and the wiring pattern.

U.S. Patent Application Serial No. **10/717,591**  
Amendment filed September 29, 2006  
Reply to OA dated May 30, 2006

**Claim 6 (Previously Presented):** The electronic parts packaging structure according to claim 1, wherein a bump of overlying electronic parts is mounted on the wiring pattern which is connected to the connection pad of the electronic parts by a flip-chip mounting.

**Claim 7 (Previously Presented):** The electronic parts packaging structure according to claim 1, wherein a thickness of the electronic parts is set to about 150  $\mu\text{m}$  or less.

**Claim 8 (Withdrawn):** A method of manufacturing an electronic parts packaging structure comprising the steps of:

preparing an electronic parts, having a connection pad that is constructed by a laminated film to have an etching stopper film as an uppermost film;

mounting the electronic parts on a mounted body to direct the connection pad upward;

forming an insulating film to cover the electronic parts;

forming a via hole by etching at least a predetermined portion of the insulating film on the connection pad by a laser; and

forming a wiring pattern connected to the connection pad via the via hole.

**Claim 9 (Withdrawn):** A method of manufacturing an electronic parts packaging structure according to claim 8, wherein the etching stopper film is a member selected from the group consisting of a copper film, a gold film, a silver film and a conductive paste film.

**Claim 10 (Withdrawn):** A method of manufacturing an electronic parts packaging structure according to claim 8, wherein the connection pad is constructed by the laminated film selected from a group consisting of aluminum film/nickel film/copper film, aluminum film/nickel film/gold film, aluminum film/nickel film/copper film/gold film, aluminum film/nickel film/silver film, aluminum film/chromium film/copper film, aluminum film/conductive paste film, aluminum film/titanium film/conductive paste film, aluminum film/chromium film/conductive paste film, and aluminum film/titanium film/copper film, which are formed sequentially from a bottom respectively, and an uppermost film of the connection pad functions as the etching stopper layer upon forming the via hole by the laser.

**Claim 11 (Withdrawn):** A method of manufacturing an electronic parts packaging structure according to claim 10, wherein the connection pad is formed of the aluminum film/nickel film/copper film, the aluminum film/nickel film/gold film, the aluminum film/nickel film/copper film/gold film, or the aluminum film/nickel film/silver film, and

the step of preparing the electronic parts includes the steps of,

forming selectively the nickel film on an aluminum pad of the semiconductor wafer having the aluminum pad by a electroless plating,

forming selectively the copper film, the gold film, the copper film/gold film, or the silver film on the nickel film by the electroless plating to form the connection pad,

reducing a thickness by grinding a back surface of the semiconductor wafer, and  
dicing the semiconductor wafer to get the electronic parts.

**Claim 12 (Withdrawn):** A method of manufacturing an electronic parts packaging structure according to claim 10, wherein the connection pad is formed of the aluminum film/chromium film/copper film, or the aluminum film/titanium film/copper film, and

the step of preparing the electronic parts includes the steps of,

forming sequentially the chromium film or the titanium film, and the copper film on a semiconductor wafer having an aluminum pad,

patterning the copper film, and the chromium film or the titanium film to leave the chromium film or the titanium film, and the copper film on the aluminum pad and to thereby form the connection pad,

reducing a thickness by grinding a back surface of the semiconductor wafer, and  
dicing the semiconductor wafer to get the electronic parts.

**Claim 13 (Withdrawn):** A method of manufacturing an electronic parts packaging structure according to claim 10, wherein the connection pad is formed of the aluminum film/chromium film/copper film, or the aluminum film/titanium film/copper film, and

the step of preparing the electronic parts includes the steps of,

forming a resist film having an opening portion on an aluminum pad of a semiconductor

U.S. Patent Application Serial No. **10/717,591**  
Amendment filed September 29, 2006  
Reply to OA dated May 30, 2006

wafer having the aluminum pad,

forming sequentially the chromium film or the titanium film, and the copper film on the resist film and the aluminum pad,

removing the resist film, and lifting off the chromium film or the titanium film, and the copper film on the resist film to leave selectively the chromium film or the titanium film, and the copper film on the aluminum pad and to thereby form the connection pad,

reducing a thickness by grinding a back surface of the semiconductor wafer, and

dicing the semiconductor wafer to get the electronic parts.

**Claim 14 (Withdrawn):** A method of manufacturing an electronic parts packaging structure according to claim 10, wherein the connection pad is formed of the aluminum film/chromium film/copper film, or the aluminum film/titanium film/copper film, and

the step of preparing the electronic parts includes the steps of,

forming sequentially the chromium film or the titanium film, and a first copper film on a semiconductor wafer having an aluminum pad,

forming a resist film having an opening portion on a portion on the first copper film, which corresponds to the aluminum pad,

forming a second copper film in the opening portion of the resist film by an electroplating,

removing the resist film, and then etching the first copper film, and the chromium film or the titanium film by using the second copper film as a mask to thereby form the connection pad,

U.S. Patent Application Serial No. **10/717,591**  
Amendment filed September 29, 2006  
Reply to OA dated May 30, 2006

reducing a thickness by grinding a back surface of the semiconductor wafer, and  
dicing the semiconductor wafer to get the electronic parts.

**Claim 15 (Withdrawn):** A method of manufacturing an electronic parts packaging structure according to claim 10, wherein the connection pad is formed of the aluminum film/conductive paste film, the aluminum film/titanium film/conductive paste film, or the aluminum film/chromium film/conductive paste film, and

the step of preparing the electronic parts includes the steps of,

forming the conductive paste film by coating selectively conductive past material on an aluminum pad of a semiconductor wafer having the aluminum pad, or on the titanium film or the chromium film formed on the aluminum pad,

reducing a thickness by grinding a back surface of the semiconductor wafer, and  
dicing the semiconductor wafer to get the electronic parts.

**Claim 16 (Withdrawn):** A method of manufacturing an electronic parts packaging structure according to claim 8, wherein the mounted body is a base substrate having a wiring pattern thereon or a structural body in which an insulating film and a wiring pattern are laminated by a predetermined number on the base substrate,

in the step of forming the via hole, the via hole is formed simultaneously in a predetermined portion of the insulating film on the wiring pattern under the electronic parts, and

U.S. Patent Application Serial No. **10/717,591**  
Amendment filed September 29, 2006  
Reply to OA dated May 30, 2006

in the step of forming the wiring pattern connected to the connection pad, the wiring pattern connected to the connection pad is formed to be connected electrically to the wiring pattern under the electronic parts via the via hole.

**Claim 17 (New):** An electronic parts packaging structure according to claim 1, wherein the connection pad is constructed by the laminated film selected from a group consisting of aluminum film/nickel film/copper film, aluminum film/nickel film/gold film, aluminum film/nickel film/copper film/gold film, aluminum film/nickel film/silver film, aluminum film/chromium film/copper film, aluminum film/conductive paste film, aluminum film/titanium film/conductive paste film, aluminum film/chromium film/conductive paste film, and aluminum film/titanium film/copper film, which are formed sequentially from a bottom respectively.